

Hybrid predictive control strategy for a low-cost converter-fed IM drive

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Abstract: The converter consisting of a single-phase half-bridge rectifier and four-switch three-phase inverter is a low-cost power converter with complicated operating constraints. It is difficult to control by the conventional strategies. This study proposes a hybrid predictive control strategy with dual loops for this converter. In the outer loop, a proportional–integral controller is designed to regulate the dc-link voltage, capacitor voltage balancing, and the speed and flux of induction motors (IM). Also, in the inner loop, the finite set model predictive control is employed to control the ac input current and stator currents of the IM. The major advantages of the control strategy include: (i) easy to deal with complicated constraints and manage multiple control targets; (ii) without the need of modulators; (iii) good dynamic response. The simulation and experimental results indicate that the proposed method can guarantee the stable operation and good performance.

1 Introduction

The single-phase input three-phase output converter-fed induction motor (IM) drive has attracted much attention due to its advantages of low cost, simple structure, high power density, and efficiency [1, 2]. Thus, it has been found widespread household application in various forms. Typical applications include the fans, air-compressors, pumps etc.

In order to decrease the costs and improve the reliability of power conversion systems, some simplified topologies of single-phase input three-phase output converters are proposed [3]. In Fig. 1a, the third bridge arm is multiplexed between converters A and B. Fig. 1b shows a more simplified converter where the capacitor bridge arm is multiplexed between converters A and B. Compared the converter in Fig. 1a, the converter in Fig. 1b requires fewer switching devices. However, the coupling and constraints are enhanced, which increases control difficulty of the converter.

Lots of control methods for the single-phase input three-phase output converter have been proposed. In most cases, the rectifier part and the inverter part are controlled separately, while the way to treat them together has not been reported. Usually, the control architecture of the converter could be divided into modulation and

control. In the rectifier part, the current control is very essential to obtain good performance. The hysteresis control and carrier-based modulation methods have been proposed [4–7]. The hysteresis current control is simple, but the frequency is unfixed and current total harmonic distortion (THD) is relatively high. The equivalence of space vector modulation (SVM) and carrier-based pulse width modulation (PWM) in three-phase rectifier is studied in detail [3]. On the basis of the equivalence, a carrier-based modulation method is proposed [4]. To improve efficiency, some PWM methods are presented. In order to improve the output characteristics of the four-switch three-phase inverter, the corresponding PWM modulation schemes are proposed in [5–9]. The calculation expression of the power tube's switching time is proposed in [10, 11], but only linear processing. Consider the dead zone effect, the switching time compensation of the power switch tube is proposed in the literature [12]. A specific harmonic injection method is used to improve the voltage utilisation to a certain extent in [13], but the calculation is complex and lacks a clear analytic expression. To mitigate the effects of the capacitor voltage fluctuation, several papers were published. An adaptive SVM approach was proposed to compensate the dc-link voltage ripple in a four-switch three-phase inverter [14]. From the perspective of source impedance and

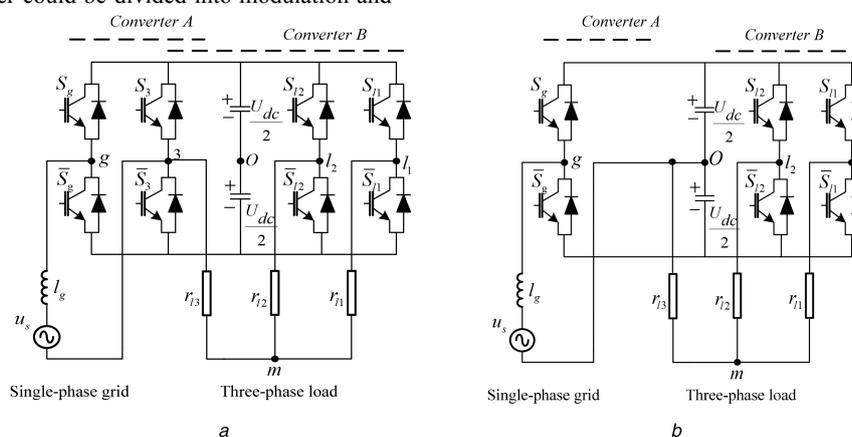


Fig. 1 Simplified topological circuit

(a) Single-phase input three-phase output full-bridge simplified topology, (b) Single-phase input three-phase output half-bridge simplified topology

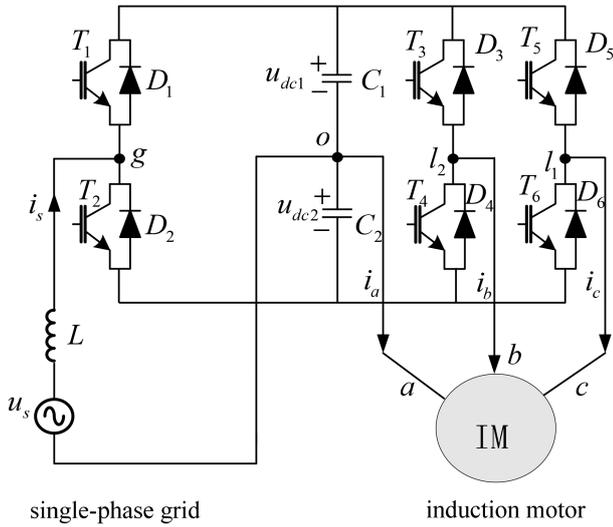


Fig. 2 Topology with reduced switches converter

the voltage variation caused by the current flowing through the capacitor, a current distortion compensation scheme is proposed [15]. In [16], a compensation method by adjusting switching times considering the capacitor voltage fluctuation is presented. The works mentioned earlier were dedicated to obtain the balanced three-phase currents of the inverter, but the inherent constraints were not considered.

As well known, the traditional model predictive control (MPC) has the advantage of handling constraints [17]. However, the general MPC is not much suitable for power electronic circuits which demands high performance of real time [18]. Recently, a finite set MPC (FS-MPC) scheme has been proposed for power converters [18–20]. The FS-MPC algorithm, which is easy to understand and flexible to control, can fully consider the constraint conditions of system and easily deal with them. In addition, it also can achieve various objectives by changing the form of the cost function, weight factor, and the number of variables. With multi-variable control capability, the rectifier and inverter part can be controlled together. The FS-MPC control treats the power converter as a discrete and non-linear actuator. In FS-MPC system, through a single controller to realise the control actions, which can select the status online from all possible states [21, 22].

In this study, a hybrid predictive control strategy which combines the FS-MPC and proportional–integral (PI) control is applied to control single-phase half-bridge rectifier and four-switch three-phase inverter. It enables the system to achieve the stable output, and can meet the high flexibility. The remainder of this paper is organised as follows: Section 2 presents the system model of the single-phase half-bridge rectifier and four-switch three-phase inverter. Section 3 presents the corresponding discrete model. The proposed control scheme is described in Section 4. In Section 5, the simulation and experimental results are shown, and finally, the conclusion is presented in Section 6.

2 System modelling

The studied single-phase input three-phase output converter is shown in Fig. 2, which is made up of six insulated gate bipolar transistors (IGBTs). IGBT T_1 , T_2 and capacitor C_1 , C_2 together constitute a single-phase half-bridge rectifier circuit, the remaining four IGBTs and the capacitor C_1 , C_2 together form a low-cost four-switch three-phase inverter circuit. The rectifier provides the dc-link voltage for the inverter and the inverter is followed by a three-phase induction motor.

To facilitate modelling, denote S_i ($i = 1, 2, 3, 4, 5, 6$) as the switching state of the switch T_i ($i = 1, 2, 3, 4, 5, 6$) of the converter. Then, the dynamic equations of the input current, the two dc-link capacitor voltages, and the stator current can be obtained as follows:

$$L \frac{di_s}{dt} = u_s - S_1 u_{dc1} + (1 - S_1) u_{dc2} \quad (1)$$

$$C \frac{du_{dc1}}{dt} = S_1 i_s - S_3 i_b - S_5 i_c \quad (2)$$

$$C \frac{du_{dc2}}{dt} = C \frac{du_{dc1}}{dt} - i_a - i_s \quad (3)$$

$$L_\delta \frac{di_a}{dt} = u_{aN} - e_a - i_a r_s \quad (4)$$

$$L_\delta \frac{di_b}{dt} = u_{bN} - e_b - i_b r_s \quad (5)$$

$$L_\delta \frac{di_c}{dt} = u_{cN} - e_c - i_c r_s \quad (6)$$

where L is the grid-side inductance, and i_s and u_s are the ac source current and voltage, respectively. $C_1 = C_2 = C$ is the capacitor of the dc-link, u_{dc1} , u_{dc2} are the upper and the lower dc-link voltages. u_{iN} , i_i , and e_i ($i = a, b, c$) are the stator phase voltage, phase current, back electromotive force of the IM, respectively. L_δ and r_s are the leakage inductance and stator resistance of the motor, respectively.

The stator phase voltage u_{aN} , u_{bN} , and u_{cN} of the induction motor can be expressed as

$$\begin{cases} u_{aN} = \frac{2}{3} u_{dc2} - \frac{1}{3} S_3 u_{dc} - \frac{1}{3} S_5 u_{dc} \\ u_{bN} = \frac{2}{3} S_3 u_{dc} - \frac{1}{3} S_5 u_{dc} - \frac{1}{3} u_{dc2} \\ u_{cN} = \frac{2}{3} S_5 u_{dc} - \frac{1}{3} S_3 u_{dc} - \frac{1}{3} u_{dc2} \end{cases} \quad (7)$$

where u_{dc} is the total dc-link voltage.

For convenience, the equations formulated in abc coordinate should be transformed to $\alpha\beta$ coordinate. As equations in (4–6) are general expressions, the back electromotive force information should be formulated concretely. Thus, the dynamic equations for stator currents of the induction motor are rewritten as [23, 24]

$$\begin{cases} \frac{di_\alpha}{dt} = -\gamma i_\alpha + \alpha \beta \psi_{r\alpha} + \beta p \omega \psi_{r\beta} + \frac{1}{\sigma} u_\alpha \\ \frac{di_\beta}{dt} = -\gamma i_\beta + \alpha \beta \psi_{r\beta} - \beta p \omega \psi_{r\alpha} + \frac{1}{\sigma} u_\beta \end{cases} \quad (8)$$

where i_α and i_β are the stator α, β -axis currents; u_α and u_β represent the stator α, β -axis voltages; $\psi_{r\alpha}$ and $\psi_{r\beta}$ are the rotor α, β -axis flux; ω and p the rotor angular speed and the number of pole pairs, respectively. Constants related to electrical and mechanical parameters of IM are defined as: $\sigma = L_s(1 - (L_m^2)/(L_s L_r))$, $\gamma = (r_s/\sigma) + \alpha L_m \beta$, $\alpha = (r_r)/(L_r)$, $\beta = (L_m)/(\sigma L_r)$, r_s , r_r , L_s , L_r are stator/rotor resistance and inductance, respectively. L_m is the mutual inductance.

In (8), the rotor flux expressions are given as follows:

$$\begin{cases} \psi_{r\alpha} = \frac{1}{T_r s + 1} (L_m i_\alpha - \omega T_r \psi_{r\beta}) \\ \psi_{r\beta} = \frac{1}{T_r s + 1} (L_m i_\beta + \omega T_r \psi_{r\alpha}) \end{cases} \quad (9)$$

$$\psi_r = \sqrt{\psi_{r\alpha}^2 + \psi_{r\beta}^2} \quad (10)$$

$$\theta = \arctan(\psi_{r\beta}/\psi_{r\alpha}) \quad (11)$$

where T_r is the rotor electromagnetic time constant, and ψ_r and θ are the amplitude and angle of rotor flux.

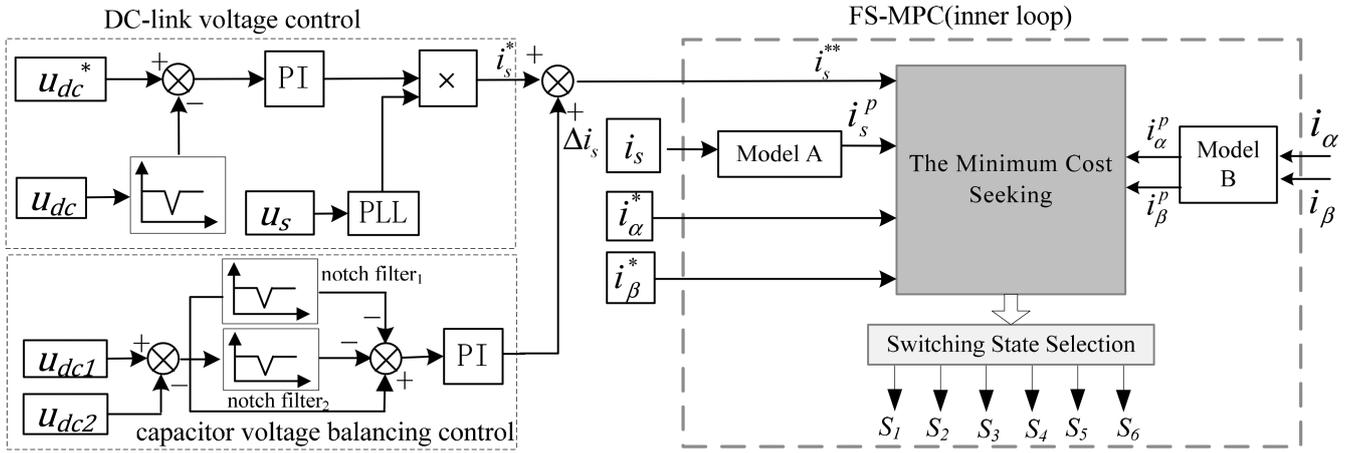


Fig. 3 Control diagram of the proposed control strategy

3 Discrete model

Assume that the sampling period t_s is relatively small. The forward Euler method is used to discretise the system. Then, the resulted discrete-time equations of (1), (2), (3), and (8) are described as

$$L \frac{i_s(k+1) - i_s(k)}{t_s} = u_s(k) - S_1 u_{dc1}(k) + (1 - S_1) u_{dc2}(k) \quad (12)$$

$$C \frac{u_{dc1}(k+1) - u_{dc1}(k)}{t_s} = S_1 i_s(k) - S_3 i_b(k) - S_5 i_c(k) \quad (13)$$

$$C \frac{u_{dc2}(k+1) - u_{dc2}(k)}{t_s} = C \frac{u_{dc1}(k+1) - u_{dc1}(k)}{t_s} - i_s(k) - i_a(k) \quad (14)$$

$$\begin{cases} \frac{i_\alpha(k+1) - i_\alpha(k)}{t_s} = -\gamma i_\alpha(k) + \alpha \beta \psi_{r\alpha}(k) + \beta p \omega \psi_{r\beta}(k) + \frac{1}{\sigma} u_\alpha(k) \\ \frac{i_\beta(k+1) - i_\beta(k)}{t_s} = -\gamma i_\beta(k) + \alpha \beta \psi_{r\beta}(k) - \beta p \omega \psi_{r\alpha}(k) + \frac{1}{\sigma} u_\beta(k) \end{cases} \quad (15)$$

Then, the predicted values i_s^p , u_{dc1}^p , u_{dc2}^p , i_α^p , and i_β^p at instant $k+1$ are obtained, respectively

$$i_s^p = \frac{t_s}{L} [u_s(k) - S_1 u_{dc1}(k) + (1 - S_1) u_{dc2}(k)] + i_s(k) \quad (16)$$

$$u_{dc1}^p = \frac{t_s}{C} [S_1 i_s(k) - S_3 i_b(k) - S_5 i_c(k)] + u_{dc1}(k) \quad (17)$$

$$u_{dc2}^p = \frac{t_s}{C} [(S_1 - 1) i_s(k) - S_3 i_b(k) - S_5 i_c(k) - i_a(k)] + u_{dc2}(k) \quad (18)$$

$$\begin{cases} i_\alpha^p = i_\alpha(k) + t_s \left[-\gamma i_\alpha(k) + \alpha \beta \psi_{r\alpha}(k) + \beta p \omega \psi_{r\beta}(k) + \frac{1}{\sigma} u_\alpha(k) \right] \\ i_\beta^p = i_\beta(k) + t_s \left[-\gamma i_\beta(k) + \alpha \beta \psi_{r\beta}(k) - \beta p \omega \psi_{r\alpha}(k) + \frac{1}{\sigma} u_\beta(k) \right] \end{cases} \quad (19)$$

4 Proposed control scheme

The targets of the whole control system are: (i) to achieve the unit power factor operation for the rectifier; (ii) to maintain the capacitor voltage balance; (iii) to regulate the speed of the IM. Fig. 3 shows the proposed control block diagram, which involves an outer loop control and an inner loop control.

4.1 Outer loop control

The outer loop controller of the rectifier is divided into two parts. One is in charge of regulating the output dc-link voltage. The other takes charge of maintaining the capacitor voltage balancing.

- DC-link voltage control:** To regulate the output dc-link voltage, a PI control is used here. The output of the PI controller is the reference for amplitude of input current. After getting the current amplitude, by combining the phase information from phase locking loop yields the input current reference. In a single-phase rectifier, its inherent ripple power at twice the grid frequency will result in second ripple on dc-link voltage. Also, the second voltage ripple may distort the input current of the rectifier further. To eliminate the negative effect, a notch filter with the notch frequency of 100 Hz is inserted as shown in Fig. 3.
- Capacitor voltage balancing control:** Unbalanced capacitor voltages will degrade the system performance, so it is important to maintain the capacitor voltage balancing. The reasons for unbalanced capacitor voltages mainly are: (i) the used two capacitors are inconsistent; (ii) the sum of i_s and i_a contains dc components due to improper control. If the used capacitors are inconsistent, they will have different rate of self-discharge. Even if the sum of i_s and i_a do not contain any dc component, unbalanced capacitor voltage phenomenon will appear.

Assume that the system is steady state and i_s , i_a are expressed as

$$\begin{cases} i_s = I_p \sin \omega_i t \\ i_a = I_m \sin \omega_o t \end{cases} \quad (20)$$

where I_p and ω_i denote the input current peak and angular frequency, and I_m and ω_o the stator current peak and angular frequency.

According to (2) and (3), the error of the two capacitor voltages is derived as

$$\Delta u = u_{dc12(0)} - \frac{I_p}{\omega_i C} \cos \omega_i t - \frac{I_m}{\omega_o C} \cos \omega_o t \quad (21)$$

where $u_{dc12(0)}$ is the initial error of the two capacitor voltages.

From (21), the fluctuation in the error of the two capacitor voltages is inevitable. Thus, what we can do is to eliminate the dc component of the capacitor voltage error. Clearly, the second and the third term of (21) must be filtered out to abstract the dc component. The way to abstract the dc component is shown in Fig. 3, where two notch filters are used. Notch filter₁ is the same as that in dc-link voltage control; Notch filter₂ is a notch filter with the notch frequency being equal to ω_o . The abstracted dc component will be processed by the capacitor voltage balancing controller (PI), then a correction term Δi_s , which will revise the input current reference i_s^* to realise the capacitor voltage balancing is yielded. The revised input current reference current i_s^{**} is expressed as

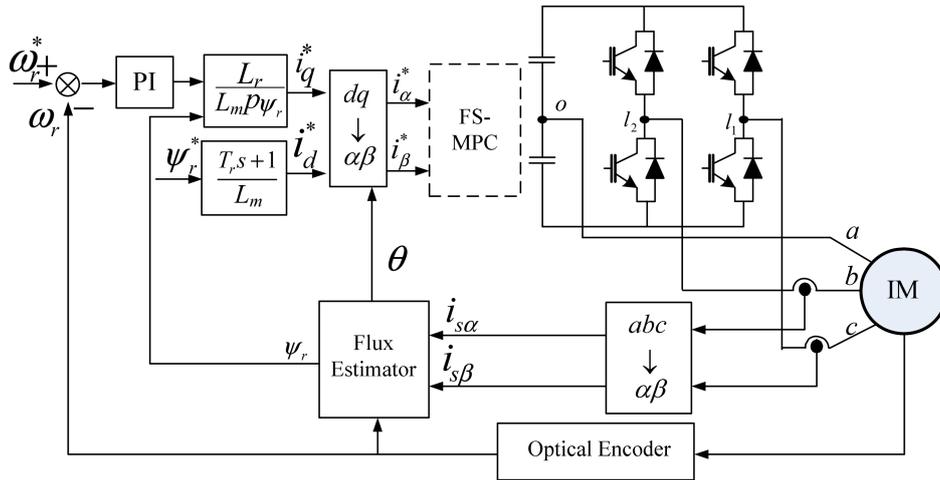


Fig. 4 External loop control diagram of induction motor

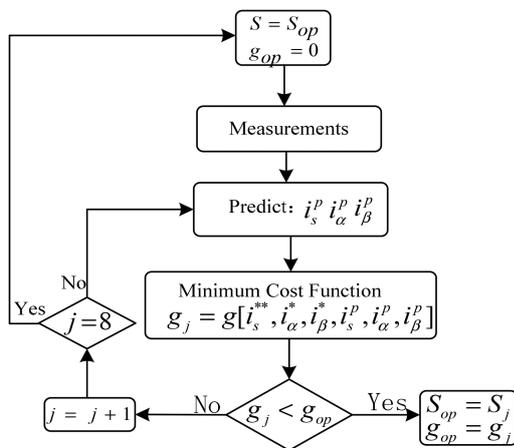


Fig. 5 Flowchart of the predictive control

$$i_s^{**} = i_s^* + \Delta i_s \quad (22)$$

iii. *Outer loop control for induction motor:* The outer loop control for induction motor is shown in Fig. 4, where the speed controller is a PI controller. The classic rotor field orientation control is used, thus the dq -coordinate system is aligned with the rotor flux to achieve the decoupling control of electromagnetic torque and rotor flux linkage.

4.2 Inner loop FS-MPC

FS-MPC is used to control the input current of the rectifier and the stator currents of the induction motor. There are two important steps in FS-MPC: establishing cost function and selecting optimal

Table 1 Parameters used in the simulations and experiments

Parameter	Value
input filtering inductance L	3 mH
input voltage	125 V(amplitude)
dc-link capacitor C_1, C_2	6800 μ F
mutual inductance L_m	0.5 H
R_s, L_s	6.4 Ω , 0.5375 H
R_r, L_r	4.8 Ω , 0.5375 H
rated power	1.1 KW
rated voltage	380 V
rated current	2.89 A
rated angular speed	145.5 rad s^{-1}

switching state. Based on the discretised model and the control objective, the cost function of the rectifier side can be set as

$$g_1 = |i_s^{**} - i_s^p|^2 \quad (23)$$

Similarly, the cost function of the inverter side is expressed as

$$g_2 = |i_\alpha^* - i_\alpha^p|^2 + |i_\beta^* - i_\beta^p|^2 \quad (24)$$

For this multi-objective optimisation problem, the weighted sum method is a simple and effective method. Therefore, g_1 and g_2 are added together to obtain the total minimum cost function

$$g = g_1 + \lambda g_2 \quad (25)$$

where λ is a weighting factor. The weighting factor is important for system performance. Usually, it is selected by trial and error method [25].

Since there are only eight possible switching combinations in this converter, and the computation burden is relatively low, the exhaust algorithm is used to obtain the optimal control. Fig. 5 shows the flowchart of the proposed algorithm.

5 Simulation and experimental research

5.1 Simulation results

In order to verify the feasibility and correctness of the hybrid predictive control for the system, some simulations have been carried out. The related parameters are listed in Table 1. The sampling period of the system is 40 μ s, and the weighting factor in (25) is set to be $\lambda = 1.5$. The inertia, flux reference, and angular speed reference of IM are $J = 0.1$ kg m^2 , $\psi_r^* = 0.96$ Wb, $\omega_r^* = 31$ rad s^{-1} , respectively.

The single-phase half-bridge rectifier converts the ac voltage of 125 V/50 Hz into the dc voltage of 300 V. The induction motor starts up without load and the load with torque $T_L = 4$ N m is connected at 0.6 s. In this case, the related simulation results are illustrated in Fig. 6. Fig. 6a shows the voltage and current waveforms of the single-phase grid in the steady-state operation. As seen, the unity power factor is almost realised in the rectifier. In Fig. 6b, the dc-link voltage is regulated at the desired value. Meanwhile, the capacitor voltage u_{dc1} and u_{dc2} are approximately the same, which means the midpoint voltage is balanced by the proposed control scheme. Fig. 6c illustrates the waveforms of stator currents. As seen, the starting current is three to four times higher than that in steady state. Since the motor is loaded at $t = 0.6$ s, the stator currents increase suddenly. Fig. 6d shows the waveforms of the angular speed and torque of the IM. It can be observed that the motor reaches the given angular speed before $t = 0.4$ s and then maintains a constant even with sudden load. The

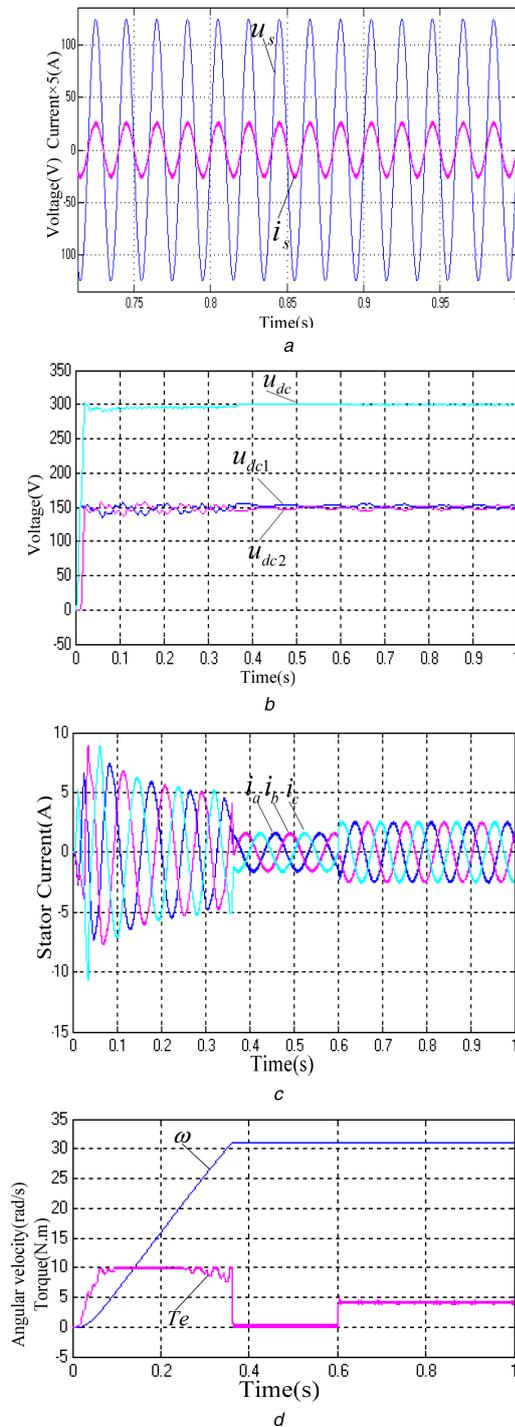


Fig. 6 Simulation results of the system
(a) Input voltage and input current, (b) Capacitor voltages, (c) Stator currents, (d) Speed and torque

torque curve shows that the starting torque is very large, then decreases gradually. When motor loads at $t=0.6$ s, the electromagnetic torque rises to 4 N m, and the load torque is balanced.

To test the response of the dc-link voltage, a step change of dc-link voltage reference from 300 to 400 V is made at $t=0.4$ s. The simulation results are shown in Fig. 7. As shown in Fig. 7a, the capacitor voltages reach a new steady-state quickly. Also, the two capacitors voltage waveforms are nearly coincided with each other, which demonstrates the feasibility of the proposed voltage balancing control scheme.

From Figs. 7b and c, it can be seen that the sinusoidal steady stator currents and torque of low ripple are obtained under the proposed control scheme.

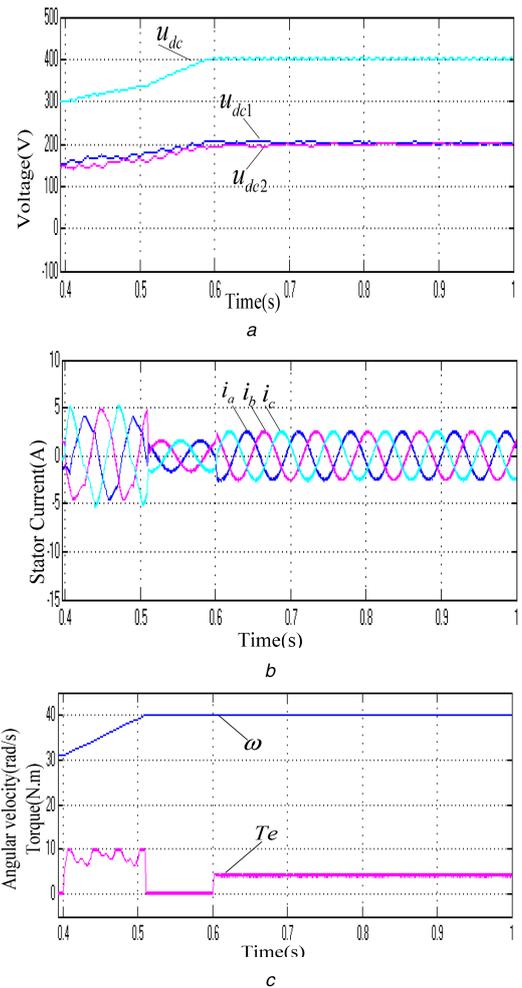


Fig. 7 Simulation result with step change of dc-link voltage from 300 to 400 V

(a) Capacitor voltages, (b) Stator currents, (c) Speed and torque

5.2 Experimental results

The proposed control algorithm is implemented in a TMS320F28335 DSP controller board. The parameters of the experimental system are the same with those in simulation and are shown in Table 1. The single-phase half-bridge rectifier is connected to the power grid through an auto-transformer, and the three-phase induction motor is driven by the three-phase four-switch inverter. Also, the dc-link voltage reference is set to 300 V. The motor starts up at no-load, after 0.35 s, a load of 4 N m is connected to it. The measured experimental waveforms are shown in Fig. 8. Fig. 8a shows the waveforms of the voltage and current of the grid. It can be seen that the grid current is in phase with the grid voltage; the unit power factor is basically realised.

The measured voltages of the two capacitors are shown in Fig. 8b. At first, the voltage on the capacitors C_1 and C_2 is maintained at 125 V by the uncontrolled diode rectifier. During start up, the voltage increase from 125 to 150 V gradually, which makes the total dc-link voltage follow its expected value of 300 V.

Figs. 8c and d show the experimental results of the induction motor, which agree well with those in the simulations. The speed is well controlled without any overshoot. The three-phase stator currents are sinusoidal, and the response of stator currents to sudden loads is very fast due to the predictive control. Meanwhile, the torque ripple is small in steady state.

The grid current and stator currents are analysed and the results are given in Table 2. The THDs are relatively low within 5% indicating the good performance of the proposed scheme.

To test the dynamic response of the dc-link voltage, its reference is changed from 300 to 400 V at $t=0.8$ s. At the same time, the speed reference is changed from 38 to 52 rad s^{-1} at $t=0.8$ s. The motor is loaded with the torque of 4 N m at $t=2$ s. The

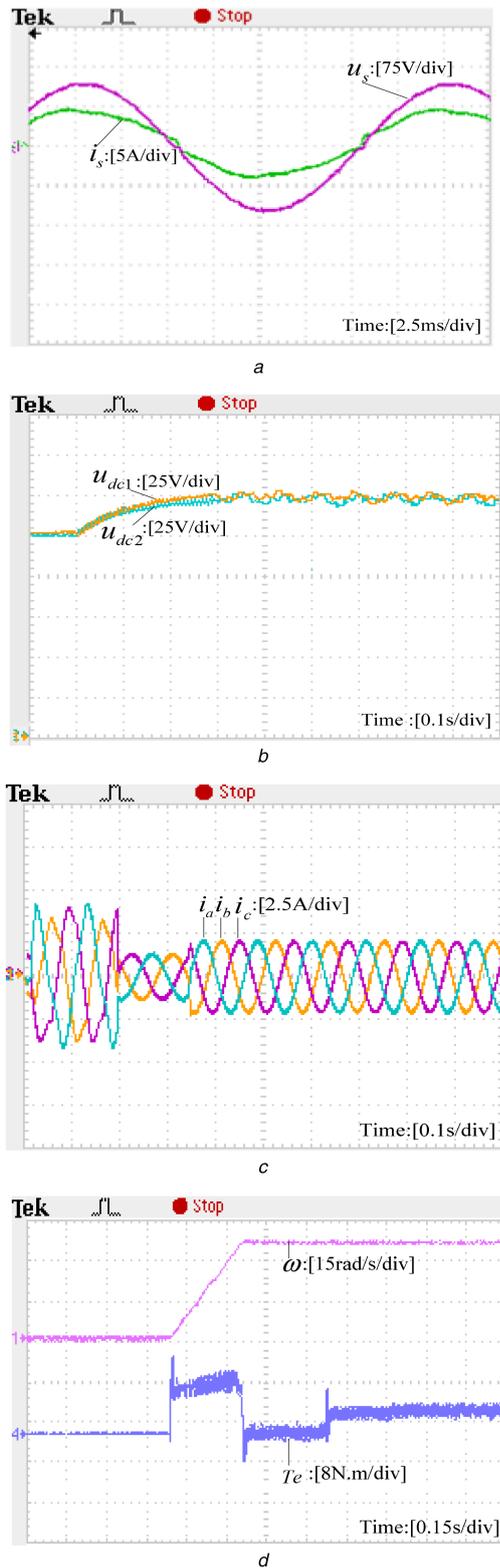


Fig. 8 Experimental results of hybrid predictive control for the low-cost converter
(a) Input voltage and current, (b) Capacitor voltages, (c) Three-phase stator currents, (d) Speed and torque

Table 2 THD and RMS value of the grid and stator currents

Grid current i_s and stator currents i_a, i_b, i_c	i_s	i_a	i_b	i_c
THD, %	3.93	3.06	3.65	3.29
RMS, A	3.23	1.45	1.40	1.42

corresponding experimental results are illustrated in Figs. 9a and b. As seen, the angular speed reaches 38 rad s^{-1} firstly and then runs

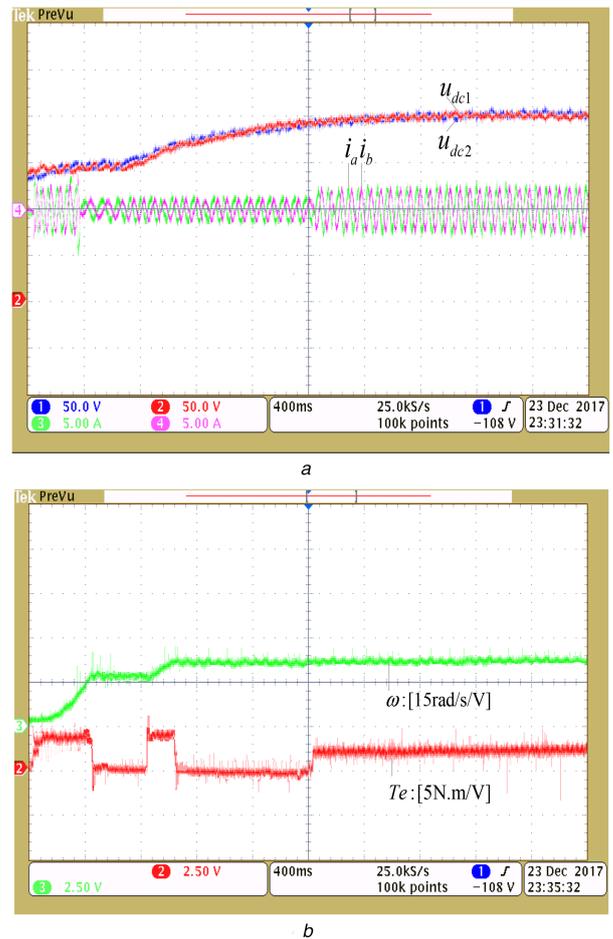


Fig. 9 Experimental results when the dc-link voltage reference is changed from 300 to 400 V
(a) Capacitor voltages and stator currents, (b) Speed and torque

at 52 rad s^{-1} steadily. Besides, the overshoot of angular speed is very small. The torque curve shows that the starting torque of the motor is very large, then decreases gradually. When motor loads at $t = 2 \text{ s}$, the electromagnetic torque rises to 4 N m to balance the load torque. The proposed control strategy shows good stator phase current waveforms. Here, it can be appreciated that the two capacitor voltages change to a new steady-state values. Moreover, the two capacitor voltage waveforms are nearly coincided with each other, which has demonstrated the dc-link two capacitor voltages is balanced when the dc-link voltage reference changes.

6 Conclusions

In this paper, a hybrid predictive control strategy is proposed, which is a combination of PI control and FS-MPC. The simulation and experimental results show that the proposed control scheme can achieve the multi-objective control efficiently. Compared with the traditional control schemes, the major advantages of the method include: (i) easy to deal with complicated constraints and manage multiple control targets; (ii) without the need of PWM modulator; (iii) good dynamic response; (iv) the computation burden is relatively low. The proposed control idea can also be applied to other drive systems.

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